

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Kazuhiro TASHIRO et al.**

Serial Number: **Not Yet Assigned**

Filed: **March 16, 2004**

For: **METHOD OF SEMICONDUCTOR DEVICE PROTECTION, PACKAGE OF SEMICONDUCTOR DEVICE**

Attorney Docket No. **042236**

Customer No.: **38834**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

March 16, 2004

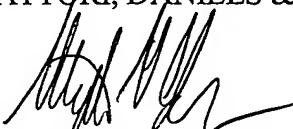
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of each of the references is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge
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Respectfully submitted,
WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP



Stephen G. Adrian
Reg. No. 32,878

1250 Connecticut Avenue, N.W., Suite 700
Washington, D.C. 20036
Tele: (202) 822-1100
Fax: (202) 822-1111
SGA/yap
Enclosures: PTO-1449; References (5)

**INFORMATION
DISCLOSURE
STATEMENT
PTO-1449**

Atty. Docket No. **042236**

Serial No. New Appln.

Applicant(s): **Kazuhiro TASHIRO et al.**

Filing Date: **March 16, 2004**

Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Translation (Yes or No)
_____	AE	7-106035	Japan	Abstract.
_____	AF	8-83658	Japan	Abstract.
_____	AG	8-88299	Japan	Abstract.
_____	AH	11-64443	Japan	Abstract.
_____	AI	2000-292487	Japan	Abstract.

OTHER DOCUMENTS

_____	AJ	
_____	AK	
Examiner		Date Considered